

STATE BOARD OF TECHNICAL EDUCATION AND TRAINING
BOARD OF EXAMINATIONS, CHENNAI 600 025.

TIME TABLE – OCTOBER 2017

DIPLOMA IN ELECTRONICS AND COMMUNICATION ENGINEERING (1040)

Date and Day	Time	Subject Code	Subjects as per the Scheme of Examination	Code No.
(M-Scheme)				
III SEMESTER				
25.10.2017 Wednesday	9.30 am – 12.30 pm	34031	Electronic Devices and Circuits	
27.10.2017 Friday	9.30 am – 12.30 pm	34032	Electrical Circuits and Instrumentation	
1.11.2017 Wednesday	9.30 am – 12.30 pm	34033	Programming in 'C'	
IV SEMESTER				
3.11.2017 Friday	2.15 pm – 5.15 pm	34041	Industrial Electronics	
6.11.2017 Monday	2.15 pm – 5.15 pm	34042	Communication Engineering	
8.11.2017 Wednesday	2.15 pm – 5.15 pm	34043	Digital Electronics	
10.11.2017 Friday	2.15 pm – 5.15 pm	34044	Linear Integrated Circuits	
V SEMESTER				
26.10.2017 Thursday	9.30 am – 12.30 pm	34051	Advanced Communication Systems	
28.10.2017 Saturday	9.30 am – 12.30 pm	34052	Microcontroller	
31.10.2017 Tuesday	9.30 am – 12.30 pm	34053	Very Large Scale Integration	
2.11.2017 Thursday	9.30 am – 12.30 pm	34071	Elective Theory – I Digital Communication	
		34272	Programmable Logic Controller *	
		34073	Electronic Systems Design	
(L-Scheme)				
VI SEMESTER				
25.10.2017 Wednesday	2.15 pm – 5.15 pm	24061	Embedded Systems	
26.10.2017 Thursday	2.15 pm – 5.15 pm	24081	Elective Theory-II Television Engineering	
		23082	Biomedical Instrumentation @	
		24083	Electronic System Design	
27.10.2017 Friday	2.15 pm – 5.15 pm	24682	Computer Hardware and Networks \$	
(K-Scheme)				
V SEMESTER				
28.10.2017 Saturday	2.15 pm – 5.15 pm	24763	Elective Theory – I Robotics and Auto Electronics **	
		14071	Digital Signal Processing	
VI SEMESTER				
26.10.2017 Thursday	2.15 pm – 5.15 pm	14081	Elective Theory – II Digital Image Processing	

@ Common with EEE

\$ Common with Information Tech

* Common with ICE

** Common with Electronics (R) (L-Scheme)